

FIG. 1

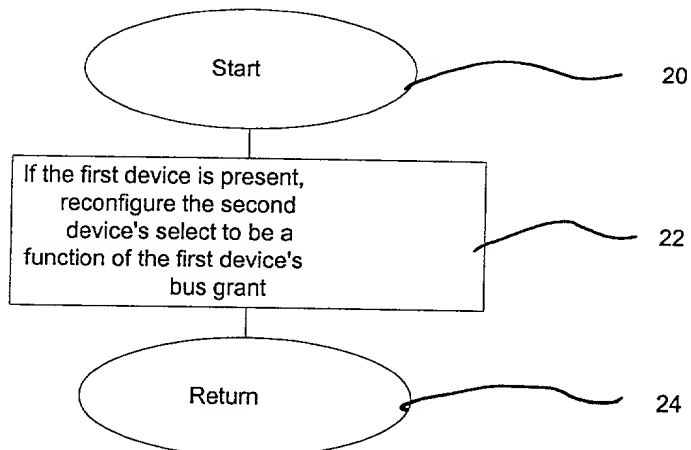
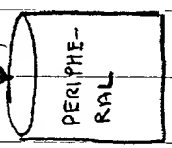
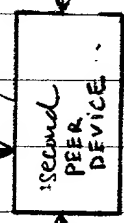
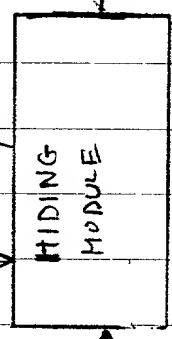
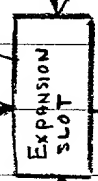
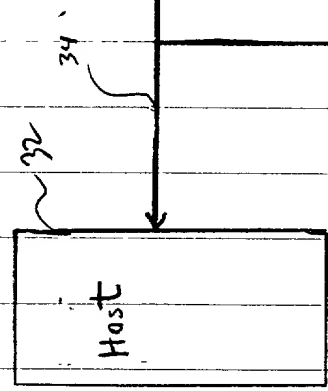
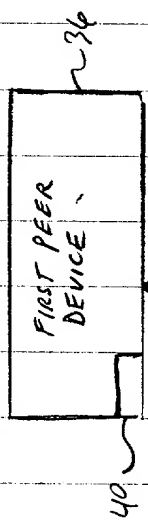


FIG. 2

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FIG. 3 is a block diagram of a system for connecting a first peer device to a host via a hidden module and an expansion slot.



MOTHERBOARD

FIG. 3

